



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew J. Adiletta Art Unit: 2183
Serial No.: 10/615,500 Examiner: Unknown

Filed : July 8, 2003

Title : PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449. Applicant has not provided copies of the US patents in accordance with the waiver posted on the USPTO website.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to deposit account 06-1050, 10559-075002.

Respectfully submitted,

Date: 4/23/04

Fish & Richardson P.C. 225 Franklin Street Boston, MA 02110-2804 Telephone: (617) 542-5070

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Denis G. Maloney Reg. No. 29,670

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Spostitute Form PTO-1449 (Modified)

U.S. Department of Commerce Patent and Trademark Office Attorney's Docket No. 10559-075002

Application No. 10/615,500

2 6 2004

formation Disclosure Statement by Applicant (Use several sheets if necessary)

Matthew J. Adiletta

Filing Date 07/08/03

Applicant

Group Art Unit 2183

(37 CFR **§ 1/9**8(b))

**U.S. Patent Documents** Examiner Desig. Document Publication Filing Date Initial Number Date Patentee Class Subclass If Appropriate ID AA 6,694,380 02/17/2004 Wolrich et al. 01/20/2004 Wolrich et al. AB 6,681,300 12/23/2003 Bernstein et al. AC 6,668,317 AD 6,667,920 06/09/2003 Wolrich et al. 12/09/2003 Wolrich et al. ΑE 6,661,794 10/07/2003 Wolrich et al. AF 6,631,462 AG 6,631,430 10/07/2003 Wolrich et al. 6,625,654 09/23/2003 Wolrich et al. AH 08/12/2003 Adiletta et al. ΑI 6,606,704 07/01/2003 Wolrich et al. AJ 6,587,906 Wolrich et al. ΑK 6,584,522 06/24/2003 Wolrich et al. AL6,577,542 06/10/2003 05/06/2003 Wolrich et al. 6,560,667 AM 04/22/2003 Adler et al. AN 6,552,826 ΑO 6,532,509 03/11/2003 Wolrich et al. AP 6,463,072 10/08/2002 Wolrich et al. 08/13/2002 6,434,145 Opsasnick et al. AQ 08/06/2002 Witkowski et al. AR 6,430,626 Adiletta et al. 07/30/2002 AS 6,427,196 Seo et al. 07/30/2002 AΤ 6,426,940 0702/2002 Habot ΑU 6,415,338 05/21/2002 Latif et al. ΑV 6,393,483 Nermirovsky et al. AW 6,389,449 05/14/2002 Allison et al. ΑX 6,373,848 04/16/2002 AY 03/19/2002 Guenthner et al. 6,360,262 ΑZ 03/12/2002 Kasper et al. 6,356,692 6,347,344 02/12/2002 Baker et al. **AAA** 

Examiner Signature			
	Exa	miner	Signature

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10559-075002	Application No. 10/615,500
Applicant Matthew J. Adiletta	
Filing Date	Group Art Unit 2183
	Applicant Matthew J. Adiletta

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	ACC	6,324,624	11/27/01	Wolrich et al.			
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•	AEE	6,298,370	10/02/2001	Tang et al.			
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	AXX	6,145,054	11/07/2000	Mehrotra et al.			
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	ABBB	6,134,665	10/17/2000	Klein et al.			

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	closure Statement oplicant	Applicant Matthew J. Adiletta	
(Use several sh	neets if necessary)	Filing Date 07/08/03	Group Art Unit 2183

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	ACCC	6,112,016	08/29/2000	MacWilliams et al.			
	ADDD	6,092,158	07/18/2000	Harriman et al.			
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	A000	6,014,729	01/11/2000	Lannan et al.			
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	ARRR	5,978,838	11/02/1999	Mohammed et al.			
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·	AIIII	5,886,992	03/23/1999	Raatikaien et al.			
	AJJJJ	5,860,158	01/12/1999	Pai et al.			
	AKKKK	5,854,922	12/29/1998	Gravenstein et al.			
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	AZZZZ	5,751,987	04/12/1998	Mahant-Shetti et al.			
	ААААА	5,745,913	04/28/1998	Pattin et al.			_
	ABBBBB	5,742,822	04/21/1998	Motomura			
	ACCCCC	5,742,782	04/21/1998	Ito et al.			
	ADDDDI	5,742,587	04/21/1998	Zornig et al.			

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1		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-075002	Application No. 10/615,500
			Applicant Matthew J. Adiletta	
	Information Disclosure S by Applicant (Use several sheets if neces	eets if necessary)	Filing Date	Group Art Unit
(37.0	CFB &1.98(b))		07/08/03	2183

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	AGGGGC	5,699,537	12/16/1997	Sharangpani et al.			
-	АНННН	5,689,566	11/18/1997	Nguyen			
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•	AJJJJJ	5,659,687	08/19/1997	Kim et al.			
	AKKKKK	5,649,157	07/15/1997	Williams			
	ALLLLL	5,644,623	07/01/1997	Gutledge			
	AMMMMI	5,630,641	05/13/1997	Perotto et al.			
_	ANNNNN	5,627,829	05/06/1997	Gleeson et al.			
•	A00000	5,623,489	04/22/1997	Cotton et al.			
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	ARRRR	5,592,622	01/07/1997	Isfeld et al.			
	ASSSSS	5,574,922	11/12/1996	James			
	ATTTTT	5,568,617	10/22/1996	Kametani			
	AUUUUU	5,557,766	09/17/1996	Takiguchi et al.			
	AVVVV	5,550,816	08/27/1996	Hardwick et al.			
	wwww	5,544,236	08/06/1996	Andruska et al.			
	AXXXXX	5,542,088	07/30/1996	Jennings, Jr. et al.			
	AYYYYY	5,542,070	07/30/1996	LeBlanc et al.			
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	ADDDDD	5,452,437	09/19/1995	Richey et al.			
	AEEEEEI	5,450,351	09/12/1995	Heddes			

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•	AIIIIII	5,404,482	04/04/1995	Stamm et al.			
	AJJJJJ	5,392,412	02/21/1995	McKenna			
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	ATTTTT	5,168,555	12/01/1992	Byers et al.			
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	AVVVVV	5,155,831	10/13/1992	Emma et al.			
	wwwww	5,142,683	08/25/1992	Burkhardt, Jr. et al.			
	AXXXXX	5,140,685	08/18/1992	Sipple et al.			
	AYYYYY	4,866,664	09/12/1989	Burkhardt, Jr. et al.			
	AZZZZZ	4,745,544	05/17/1988	Renner et al.		(	
	ААААА	4,523,272	06/11/1985	Fukunaga et al.			
	вввввв	4,514,807	04/30/1985	Nogi			
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	DDDDDI	4,130,890	12/19/1978	Adam			
	AEEEEEE	3,940,745	02/24/1976	Sajeva			
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	AIIIIII	10/726,757	12/03/2003	Wolrich et al.			
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	vvvvv	WO 01/50247	07/12/2001	WIPO					
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	YYYYY	WO 01/48596	07/05/2001	WIPO					
1	AZZZZZZ	WO 01/16782	03/08/2001	WIPO					
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~	вввввві	WO 01/16769	03/08/2001	WIPO					
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J	DDDDDD	WO 97/38372	10/16/1997	WIPO					

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Initial	/ ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	EEEEEE	WO 94/15287	07/07/1994	WIPO				
J	AFFFFFFF	EP 0 809 180	11/26/1997	Europe				
	GGGGG	EP 0 745 933	12/04/1996	Europe		•		
	инннни	EP 0 633 678	01/11/1995	Europe				
	AIIIIIIII	EP 0 464 715	01/08/1992	Europe				_
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ノ	PPPPPPPP	Haug et al., "Reconfigurable hardware as shared resource for parallel threads," IEEE Symposium on FPGAs for Custom Computing Machines, 1998.					
	20000000	Hauser et al., "Garp: a MIPS processor with a reconfigurable coprocessor," Proceedings of the 5 <sup>th</sup> Annual 1EEE Symposium on Field-Programmable Custom Computing Machines, 1997.					
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	xxxxxxx	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.					

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